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Tamara Alcaraz

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

In the application of:

Yoshinao MORIKAWA

Serial No.:

To Be Assigned

Filing Date:

Herewith

For:

REFERENCE VOLTAGE

GENERATION CIRCUIT FOR SEMICONDUCTOR MEMORY DEVICE, MEMORY READING CIRCUIT INCLUDING THE SAME, AND ELECTRONIC INFORMATION DEVICE INCLUDING THE SAME

PRELIMINARY AMENDMENT

Box Patent Application U.S. Patent and Trademark Office Washington D.C. 20231

Dear Sir:

Prior to examination on the merits, applicants respectfully request that the following amendment be entered.